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control unit processing unit faraday OR shieldi

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Patents Patents 1 - 20 on control unit processing unit faraday OR shielding OR firewall AND microchip OR microprocessor OR substrate OR

▸ **List view**

[Cover view](#)

Shielding system and method for an entertainment system for use with a ...

US Pat. 5864331 - Filed Apr 17, 1997 - General Electric Company

After the **microprocessor** 76 receives an indication from the **control** decode 80 that the button for the lamps 23 on the remote **control unit** 38 has been ...

▸ **Sort by relevance**

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Color filter repair method and apparatus, color filter, liquid crystal ...

[Sort by date \(old first\)](#)

US Pat. 5714195 - Filed Mar 29, 1995 - Canon Kabushiki Kaisha

The filter elements 100 of the respective colors are an information **processing unit** will be described below with formed on the light-**shielding** grating Wb. ...

▸ **Any status**

[Issued patents](#)

Substrate processing apparatus and substrate processing method

US Pat. 6402401 - Filed Oct 16, 2000 - Tokyo Electron Limited

12, it is suitable that a **shielding** plate 93 shown by the dashed line for ...

Consequently, temperature **control** in a solution **processing unit** group can be ...

[Applications](#)

Personal computer microprocessor firewalls for internet distributed processing

US Pat. 6167428 - Filed May 27, 1999

10A: namely, a master **control** and/or **processing unit** 93 and one or more slave **processing units** ... preferably separated by a **firewall** 50 and **microprocessor**

...

Distributed processing control system for automatic welding operation

US Pat. 5148000 - Filed Jan 4, 1990 - CRC-Evans Pipeline International, Inc.

A **control** system for a welding machine as recited for controlling a **microprocessor control unit** to 10 jn daim 9 wherein said communication link comprises a ...

Workstation for local and remote data processing

US Pat. 4665501 - Filed Sep 30, 1983 - Easrit Systems, Inc.

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Patents

Patents **1 - 20** on **network access "processing unit" "control unit" microchip OR microprocessor.** (0.08 seconds)

▸ **List view**

[Cover view](#)

[User-transparent security method and apparatus for authenticating user ...](#)

US Pat. 5721780 - Filed May 31, 1995 - Lucent Technologies, Inc.

Access to the computer **network** 200 is typically monitored and performed by ...
the **microprocessor**, and interacting with the call 35 signal **processing unit** ...

▸ **Sort by relevance**

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[Systems and apparatus for electronic communication and storage of time ...](#)

US Pat. 5734891 - Filed Jun 12, 1996

The **microprocessor** con- clock 111 of the reader **control unit** control system ...
a H8/322 **microprocessor** central **processing unit** with the **control unit** CPU. ...

▸ **Any status**

[Issued patents](#)

[Applications](#)

[Microprocessor architecture with a switch **network** and an arbitration unit ...](#)

US Pat. 5941979 - Filed Aug 21, 1997 - Seiko Epson Corporation

Each time a new address appears on the switch **network** invention will now be
described. ... which must have **access** to memory with the low memory latency. ...

[Programmable testing analyzer](#)

US Pat. 4611320 - Filed May 21, 1984 - Siemens Corporate Research and Support, Inc.

... said **microprocessor** system is an input- / 5 /output **processing unit** designed
... transfer **Processing unit** a central switch having a common **control unit** ...

[Optical **network unit** for communicating telephony and video information](#)

US Pat. 6005865 - Filed Nov 26, 1997 - Alcatel USA Sourcing, L.P.

Full service optical line unit 82 includes a dual SONET physical layer interface
90, a cell **processing unit** 92, and a **control unit** 94. ...

[Two-way AC power line communications system](#)

US Pat. 4429299 - Filed Jan 26, 1981 - Robertshaw Controls Company

The central **processing unit** The polling function performed by the microproces-
also ... the **microprocessor** combination with the direct memory **access** address

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Patents

Patents **1 - 20** on "processing unit" "control unit" microchip OR microprocessor. (0.07 seconds)

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[Cover view](#)

Central **processing unit** for use in a **microprocessor**

US Pat. 3969724 - Filed Apr 4, 1975 - The Warner & Swasey Company

In a **microprocessor** having a central **processing unit**, memories external to the ... and j. a **control unit** including: 65 I. a read only memory having a ...

▸ **Sort by relevance**

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Microprocessor with two groups of internal buses

US Pat. 5379394 - Filed Jul 13, 1990 - Kabushiki Kaisha Toshiba

1 is a structural diagram of a conventional **microprocessor** or CPU (Central **Processing Unit**). A conventional CPU 10 is mainly composed of an arithmetic ...

▸ **Any status**

[Issued patents](#)

[Applications](#)

Logically transportable **microprocessor** interface **control unit** permitting bus ...

US Pat. 4727477 - Filed Mar 22, 1985 - International Business Machines Corp.

A single-chip **microprocessor** comprising: external signal pins to which off-chip devices are connected; a microprocessing unit; and an interface **control unit** ...

Two-way AC power line communications system

US Pat. 4429299 - Filed Jan 26, 1981 - Robertshaw Controls Company

Four bits of this data code pertain to data 15 previously provided to the room **control unit** from the central **processing unit**. The final four bits of the ...

Systems and apparatus for electronic communication and storage of time ...

US Pat. 5734891 - Filed Jun 12, 1996

The **microprocessor** con- clock 111 of the reader **control unit** control system ...
a H8/322 **microprocessor** central **processing unit** with the **control unit** CPU. ...

Microprogram-coupled multiple-**microprocessor** module with 32-bit byte width ...

US Pat. 4736317 - Filed Jul 17, 1985 - Syracuse University

A microprogram-coupled multiple-**microprocessor** pointer **processing unit**, ... of said stack pointer **processing unit** in- 20 or no includes program **control unit**

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Patents

Patents 1 - 6 on firewall "processing unit" "control unit" microchip OR microprocessor. (0.05 seconds)

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[Computer system having remote wake-up function and remote wake-up method thereof](#)

US Pat. 6366957 - Filed Mar 5, 1999 - Samsung Electronics Co., Ltd.

The central **processing unit** is often described as a **microprocessor**. ... The combination of the **control unit** and the arithmetic-logic unit can be referred to

...

▸ Sort by relevance

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[Global network computers](#)

US Pat. 7024449 - Filed May 20, 1999

The system according to claim 1, wherein said **microprocessor** chip further ... further comprising a **firewall** for said at least two personal computers to ...

▸ Any status

[Issued patents](#)

[Applications](#)

[\[APPLICATION\] Method and apparatus for document markup language driven server](#)

US Pat. 9832319 - Filed Apr 9, 2001

Microprocessor 3600, comprised of a Central **Processing Unit** (CPU) 3610, memory cache ... via system bus 3635 to main memory 3640 and I/O **control unit** 3645. ...

[\[APPLICATION\] Method and apparatus for value allocation exchange](#)

US Pat. 9908058 - Filed Jul 18, 2001

Microprocessor 3600, comprised of a Central **Processing Unit** (CPU) 3610, memory cache ... via system bus 3635 to main memory 3640 and I/O **control unit** 3645. ...

[\[APPLICATION\] Interactive system for and method of automating the generation of legal ...](#)

US Pat. 9801949 - Filed Mar 7, 2001

A **microprocessor** 1300, comprised of a Central **Processing Unit** (CPU) 1305, ... The I/O interface **control unit** can be operatively coupled via an I/O local bus

...

[\[APPLICATION\] COMMUNICATION METHOD, COMMUNICATION APPARATUS, AND COMMUNICATION SYSTEM WITH](#)

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Patents

Patents 1 - 20 on secure architecture "processing unit" "control unit" microchip OR microprocessor. (0.07 seconds)

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Logically transportable **microprocessor** interface **control unit** permitting bus ...

US Pat. 4727477 - Filed Mar 22, 1985 - International Business Machines Corp.

A bus control **architecture** for microprocessors consisting of a logically ...
different **microprocessor**, said interface **control unit** being connected to memory

▸ Sort by relevance

...

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Microprocessor with an **architecture** mode **control** capable of supporting ...

US Pat. 5854913 - Filed Jun 10, 1997 - International Business Machines Corporation

The **architecture** context mechanism bit from the **architecture** mode **control unit**
108 determines which of the two interrupt and exception mechanisms, ...

▸ Any status

[Issued patents](#)

[Applications](#)

Remote network device for controlling the operation voltage of network devices

US Pat. 6381700 - Filed Jul 6, 1998 - Fukiko Yoshida

A central **processing unit** (CPU) or **microprocessor** of each computer is set in ...
the remote network device further includes a **control unit** responsive to an ...

Conditional purchase offer management system for cruises

US Pat. 6134534 - Filed Sep 4, 1997 - priceline.com Incorporated

3 is a block diagram showing the **architecture** of an illustrative secured ...
such as a central **processing unit** (CPU) 305, a random access memory (RAM) 310,

...

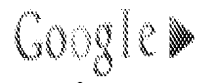
Copier/printer and method for reproduction of **secure** documents or the like

US Pat. 4980719 - Filed Jun 13, 1989 - Eastman Kodak Company

Such signals are sent to a computer controlled logic and **control unit** (LCU) 31.
... of course, depend on the **architecture** of the designated **microprocessor**. ...

Data processor

US Pat. 6023757 - Filed Jan 30, 1997 - Hitachi, Ltd.



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secure architecture "processing unit" separate

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Patents

Patents 1 - 6 on **secure architecture "processing unit" separate "control unit" microchip OR microprocessor**. (0.04 seconds)

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Microprocessor with an architecture mode control capable of supporting ...

US Pat. 5854913 - Filed Jun 10, 1997 - International Business Machines Corporation

The **architecture** context mechanism bit from the **architecture mode control unit**

108 determines which of the two interrupt and exception mechanisms, ...

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Conditional purchase offer management system for cruises

US Pat. 6134534 - Filed Sep 4, 1997 - priceline.com Incorporated

3 is a block diagram showing the **architecture** of an illustrative secured ...

such as a central **processing unit** (CPU) 305, a random access memory (RAM) 310,

...

▸ Any status

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[Applications](#)

Conditional purchase offer management system

US Pat. 6085169 - Filed Jul 8, 1997 - priceline.com Incorporated

The CPU 205 preferably includes a **control unit**, an arithmetic logic unit (ALU)

... such as a central **processing unit** (CPU) 305, a random access memory (RAM)

...

Postal meter with date check reminder means

US Pat. 4635204 - Filed Dec 8, 1982 - Pitney Bowes Inc.

Each of such units includes a dedicated central **processing unit** connected by way

of ... control lines and address lines to, in the case of the **control unit**, ...

[APPLICATION] **Methods and systems for automated loan origination, processing and approval**

US Pat. 9928036 - Filed Aug 8, 2001

A **microprocessor** 130, comprised of a Central **Processing Unit** (CPU) 132, ...

coupled via a system bus 142 to a main memory 144 and an I/O **control unit** 146.

...

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secure architecture "processing unit" separat

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Patents

Patents 1 - 20 on secure "processing unit" "control unit" microchip OR microprocessor. (0.05 seconds)

▸ List view

Logically transportable **microprocessor** interface **control unit** permitting bus ...

[Cover view](#)

US Pat. 4727477 - Filed Mar 22, 1985 - International Business Machines Corp.

Having thus described my invention, what I claim as new, and desire to **secure** by

Letters Patent is: 1. A single-chip **microprocessor** comprising: external ...

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Method and apparatus for performing **secure processing** of postal data

[Sort by date \(old first\)](#)

US Pat. 6381589 - Filed Dec 16, 1999 - Necpost Inc.

Local host computer 210 includes a **secure processing unit** 240 that 5

Control unit 440 accesses memories 410 and 412 by reading or writing on data

lines ...

▸ Any status

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Microprocessor with an architecture mode **control** capable of supporting ...

[Applications](#)

US Pat. 5854913 - Filed Jun 10, 1997 - International Business Machines Corporation

... by the branch **processing unit** 10 15 25 40 45 50 55 60 65 (BPU) 1054 in FIG.

... architecture mode **control unit** 108 determines which of the two interrupt ...

Computer system having remote wake-up function and remote wake-up method thereof

US Pat. 6366957 - Filed Mar 5, 1999 - Samsung Electronics Co., Ltd.

The central **processing unit** is often described as a **microprocessor**. ... The

combination of the **control unit** and the arithmetic-logic unit can be referred to

...

Communication line adapter for use with a communications controller

US Pat. 4504901 - Filed Sep 10, 1981 - International Business Machines Corporation

When two data bursts have been transferred to the central **control unit**, ... the

exchange of data between at least one central **processing unit** and terminals, ...

Copier/printer and method for reproduction of **secure** documents or the like

US Pat. 4980719 - Filed Jun 13, 1989 - Eastman Kodak Company

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Patents

Patents **1 - 20** on **secure processing control unit microchip OR microprocessor**. (0.08 seconds)

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Method and apparatus for performing **secure processing** of postal data

US Pat. 6381589 - Filed Dec 16, 1999 - Neopost Inc.

Local host computer 210 includes a **secure processing unit** 240 that 5 a microcontroller, a **microprocessor**, or other electronic units designed to perform

› **Sort by relevance**

...

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Real time process controller with serial I/O bus

US Pat. 4910658 - Filed Sep 27, 1988 - Eaton Leonard Technologies, Inc.

Connector 54 is configured at its other end to receive 35 and **secure** with a plurality ... Specifically, the **control unit** 20 includes a **microprocessor** 70 for

› **Any status**

...

[Issued patents](#)

[Applications](#)

Logically transportable **microprocessor** interface **control unit** permitting bus ...

US Pat. 4727477 - Filed Mar 22, 1985 - International Business Machines Corp.

A single-chip **microprocessor** comprising: external signal pins to which off-chip devices are connected; a microprocessing **unit**; and an interface **control unit** ...

Secure memory management **unit** for **microprocessor**

US Pat. 5825878 - Filed Sep 20, 1996 - VLSI Technology, Inc.

All of these transfers of information are controlled by the **secure** DMA/MMU ... A request bit in the **control** register in the DMA controller 14 is set when ...

Secure processing unit systems and methods

US Pat. 7124170 - Filed Aug 21, 2000 - Intertrust Technologies Corp.

IB, the resulting microcontroller/**microprocessor** 100 other peripherals outside of ... **control** of an LCD display, user processor or microcontroller designs. ...

Secure application card for sharing application data and procedures among a ...

US Pat. 5491827 - Filed Jan 14, 1994 - Rull HN Information Systems Inc.

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tamper resistant processing control unit micro

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Patents

Patents 1 - 20 on **tamper resistant processing control unit microchip OR microprocessor**. (0.05 seconds)

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Tamper resistant microprocessor

US Pat. 6983374 - Filed Feb 13, 2001 - Kabushiki Kaisha Toshiba

14, the second embodiment of a **tamper resistant microprocessor** according to the ... an exception **processing unit** 131, a data TLB (Table Lookup Buffer) 141, ...

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Anti-theft device with alarm screening

US Pat. 5963131 - Filed Aug 4, 1998 - Lexent Technologies, Inc.

Possession of **control unit** 22 is necessary to place theft detector 21 in its low power mode. The **tamper resistant** function of mode switch 28 prevents the ...

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[Applications](#)

Tamper-resistant, expandable communications system

US Pat. 4450481 - Filed Aug 25, 1981 - E-Com Corporation

At the downstream (terminal "b") side of **unit** Power for the transponder module ... **Microprocessor** 605 formed at the headend **control** computer 105 in order to ...

Motion sensitive anti-theft device with alarm screening

US Pat. 6133830 - Filed Jun 19, 1998 - Lexent Technologies, Inc.

Possession of **control unit** 22 is necessary to place theft detector 21 in its low power mode. The **tamper resistant** function of mode switch 28 prevents the ...

Low cost tamper-resistant method for write-once read many (WORM) storage

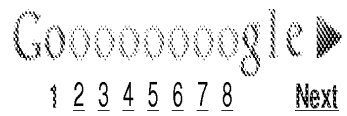
US Pat. 6272086 - Filed Nov 18, 1997 - International Business Machines Corporation

The R/W **control unit** 72 includes a **microprocessor** containing a plurality of ... by the nonconforming drive is sufficient to preclude **processing** of the disk ...

Secure processing unit systems and methods

US Pat. 7124170 - Filed Aug 21, 2000 - Intertrust Technologies Corp.

IB: the resulting microcontroller/**microprocessor** 100 other peripherals outside



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tamper resistant "processing unit" "control unit"

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Patents

Patents **1 - 1** on **tamper resistant "processing unit" "control unit" microchip OR microprocessor.** (0.03 seconds)

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[APPLICATION] CRYPTOGRAPHIC METHODS, APPARATUS AND SYSTEMS FOR STORAGE MEDIA ELECTRONIC ...

US Pat. 6848077 - Filed May 15, 1997 - InterTrust Technologies Corp.

Additionally, computer equipment 62 may include a **tamper-resistant** secure ... by

a secure **processing unit** 164, software executing on **microprocessor** 154, ...

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